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Information Disclosure Statement By Applicant	Applicant: Lines et al.	
(Use Several Sheets if Necessary)	Filing Date September 16, 2003	Group 2183

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
Ad	A1	6,038,656	03.14.00	Martin et al.			
Ad	A2	5,752,070	05.12.98	Martin et al.			
Ad	A3	6,044,061	03.28.00	Aybay et al.			
Ad	A4	5,832,303	11.03.98	Murase et al.			
Ad	A5	6,230,228	05.08.01	Eskandari et al.			
Ad	A6	5,802,055	09.01.98	Krein et al.			
Ad	A7	6,279,065	08.21.01	Chin et al.			
Ad	A8	6,301,630	10.09.01	Chen et al.			

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
Ad	C1	Andrew Matthew Lines, <u>Pipelined Asynchronous Circuits</u> , June 1995, revised June 1998, pp. 1-37.
Ad	C2	Alain J. Martin, <u>Compiling Communicating Processes into Delay-Insensitive VLSI Circuits</u> , December 31, 1985, Department of Computer Science California Institute of Technology, Pasadena, California, pp. 1-16.
Ad	C3	Alain J. Martin, <u>Erratum: Synthesis of Asynchronous VLSI Circuits</u> , March 22, 2000, Department of Computer Science California Institute of Technology, Pasadena, California, pp. 1-143.
Ad	C4	U.V. Cummings, et al. <u>An Asynchronous Pipelined Lattice Structure Filter</u> , Department of Computer Science California Institute of Technology, Pasadena, California, pp. 1-8.
Ad	C5	Alain J. Martin, et al. <u>The Design of an Asynchronous MIPS R3000 Microprocessor</u> , Department of Computer Science California Institute of Technology, Pasadena, California, pp. 1-18.
Ad	C6	U.S. Application 09/501,638, filed on February 10, 2000, entitled, "Reshuffled Communications Processes in Pipelined Asynchronous Circuits".
Ad	C7	Lee et al., "Crossbar-Based Gigabit Packet Switch with an Input-Polling Shared Bus Arbitration Mechanism", September 21, 1997, XVI World Telecom Congress Proceedings, Interactive Session 3 - Systems Technology & Engineering, pp. 435-441.
Ad	C8	Ghosh et al., "Distributed Control Schemes for Fast Arbitration in Large Crossbar Networks", March 1994, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 2, No. 1, pp. 55-67.
Examiner	Date Considered	
Amee	4/3/2006	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.